Freeform Search

Database: US Pre-Grant Publication Full-Text Database
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IBM Technical Disclosure Bulletins

Term: L2 and (((predetermin$3 or pre-determin$3 or specific$4) adj (slot$1 or cycle)) with (access$3 or read$3 or writ$3 or load$3 or

Display: 10 **Documents in Display Format:** TI **Starting with Number** 1

Generate: ○ **Hit List** ◉ **Hit Count** ○ **Side by Side** ○ **Image**

Search Clear Interrupt

Search History

DATE: Monday, March 29, 2004 Printable Copy Create Case

Set Name side by side	Query	Hit Count	Set Name result set
	DB=USPT; PLUR=YES; OP=OR		
L3	L2 and (((predetermin$3 or pre-determin$3 or specific$4) adj (slot$1 or cycle)) with (access$3 or read$3 or writ$3 or load$3 or stor$3))	24	L3
L2	711/104,105,106.ccls. or 711/167,169.ccls. or 365/222,189.01,200,193.ccls.	6537	L2
L1	(711/104,105,106.ccls. or 711/167,169.ccls. or 365/222,189.01,200,193.ccls. and (((predetermin$3 or pre-determin$3 or specific$4) adj (slot$1 or cycle)) with (access$3 or read$3 or writ$3 or load$3 or stor$3)))	0	L1

END OF SEARCH HISTORY

Freeform Search

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Term: L7 and ((slot$1 or cycle) with (access$3 or read$3 or writ$3 or load$3 or stor$3))

Display: 10 **Documents in Display Format:** TI **Starting with Number** 1

Generate: ○ **Hit List** ◉ **Hit Count** ○ **Side by Side** ○ **Image**

Search Clear Interrupt

Search History

DATE: Monday, March 29, 2004 Printable Copy Create Case

Set Name side by side	Query	Hit Count	Set Name result set
DB=USPT; PLUR=YES; OP=OR			
L8	L7 and ((slot$1 or cycle) with (access$3 or read$3 or writ$3 or load$3 or stor$3))	67	L8
L7	L4 and ((predetermin$3 or pre-determin$3 or specific$4) adj (slot$1 or cycle))	81	L7
L6	L4 and ((predetermin$3 or pre-determin$3 or specific$4) adj3 (slot$1 or cycle))	299	L6
L5	L4 and ((predetermin$3 or pre-determin$3 or specific$4) with (slot$1 or cycle))	920	L5
L4	l1 or l2 or L3	6537	L4
L3	365/222,189.01,200,193.ccls.	4818	L3
L2	711/167,169.ccls.	1179	L2
L1	711/104,105,106.ccls.	920	L1

END OF SEARCH HISTORY

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Generate Collection

L3: Entry 21 of 24 File: USPT Sep 1, 1992

DOCUMENT-IDENTIFIER: US 5144581 A
TITLE: Apparatus including atomic probes utilizing tunnel current to read, write and erase erase data

Current US Cross Reference Classification (6):
365/189.01

CLAIMS:

2. The apparatus according to claim 1, wherein said arithmetic operation is sampling of information, and said control means comprises

writing means for controlling said scanning means and said information writing means to write said information from said information generator in the small capacity recording region of said recording medium at a first timing of a predetermined cycle, and

reading means for controlling said scanning means and said information reading means to read out said information from the small capacity recording region of said recording medium at a second timing of the predetermined cycle.

L4: Entry 26 of 30 File: USPT Feb 21, 1989

DOCUMENT-IDENTIFIER: US 4807289 A
TITLE: Apparatus for recording and reproducing human speech by its analysis and synthesis

Abstract Text (1):
A speech analysis and synthesis device comprises analyzing and synthesizing means for analyzing human speech to produce analyzed data and for synthesizing human speech on the basis of the analyzed data, a dynamic RAM for memorizing the analyzed data, a refresh address counter for outputting a refresh address sequentially varying every a predetermined refresh cycle for designating a memory location of the dynamic RAM, an access address counter for outputting an access address indicative of a memory location where the analyzed data is accessed, and refresh-access means for effecting a refresh operation of a memory cell assigned to the refresh address every the predetermined refresh cycle and for providing an access to a memory cell assigned to the access address in synchronism with the predetermined refresh cycle during non-refresh cycle. The speech analysis and synthesis device enables connection of the dynamic RAM without provision of complicated peripheral circuits.

Brief Summary Text (7):
To achieve this object, there is provided an apparatus for analyzing and synthesizing human speech comprising: means for analyzing human speech to produce analyzed data and for synthesizing human speech on the basis of said analyzed data; a dynamic RAM for memorizing said analyzed data; a refresh address counter for outputting a refresh address sequentially varying every predetermined refresh cycle, said refresh address indicating a location of a memory cell to be refreshed in said dynamic RAM; an access address counter for outputting an access address indicative of a location of a memory cell where said analyzed data in said dynamic RAM is accessed; and means for effecting a refresh operation of a memory cell assigned to said refresh address every said predetermined refresh cycle and for providing an access to a memory cell assigned to said access address address in synchronism with said predetermined refresh cycle for a time period during which a refresh operation is not effected.

Current US Cross Reference Classification (1):
365/222

Generate Collection

L4: Entry 21 of 30 File: USPT Apr 4, 1995

DOCUMENT-IDENTIFIER: US 5404335 A
TITLE: Dynamic type semiconductor memory device operable in a self-refreshing mode

Brief Summary Text (43):
In the self-refreshing mode, the refreshing address counter 120 generates the refreshing address. For the periodical refreshing of the memory cells in each row in the memory cell array in the DRAM, it is necessary to operate the refreshing address counter correctly and to generate the refreshing address periodically. If refreshing address counter 120 is a ten-bit counter, the counter is required to generate the same refreshing address every 1024 cycles.

Current US Original Classification (1):
365/222

Current US Cross Reference Classification (1):
365/193